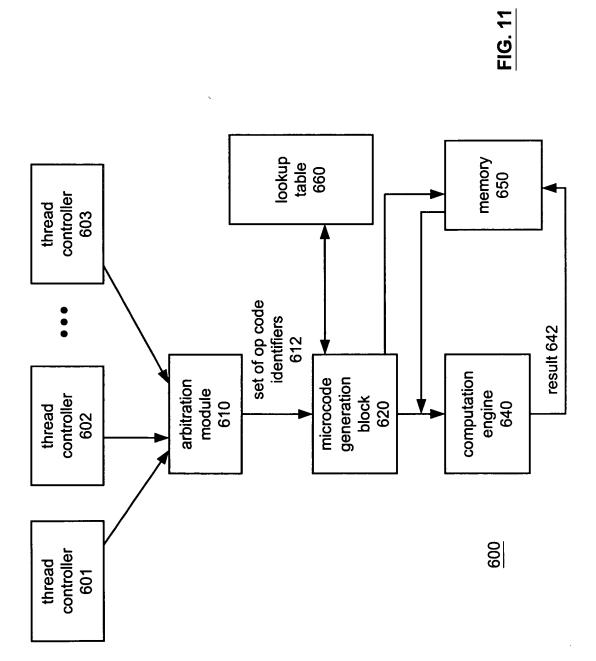


200

FIG. 10



A= input from memory1	address = Block(thread IU) + Index(light IU)  B= input from memory2  address = Block(thread ID) + Constant1	Accumulator = Accumulation Register(thread ID)				FIG. 12		
	(A*B) -> Accumulator	(C*D) -> Accumulator	(E*F) -> M1	(G*M1) -> Accumulator	(Accumulator *M2) -> M3	•••	(M3*M4)=result -> M5	lookup table 660
instruction index	언	<u> </u>		<u></u>	4		Z	ſ

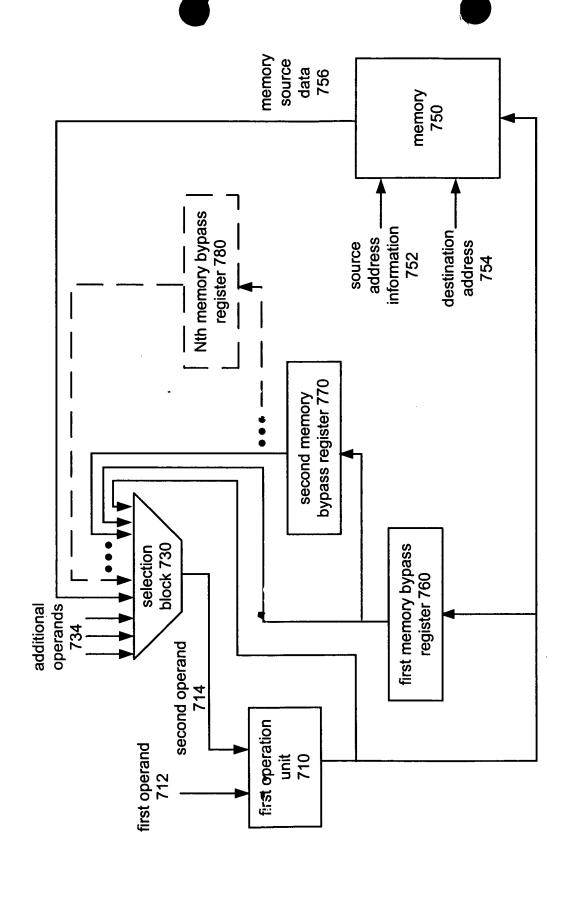


FIG. 13

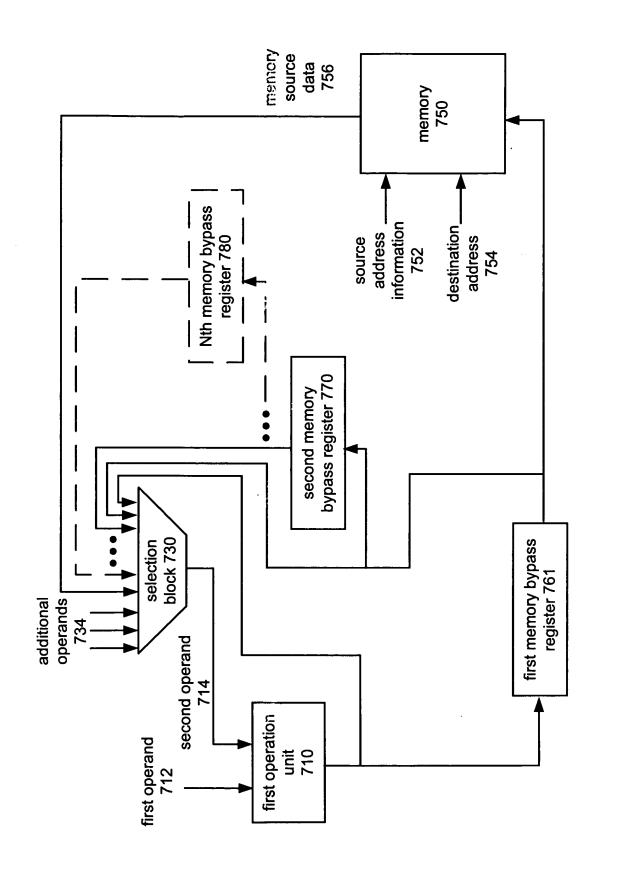


FIG. 14

701

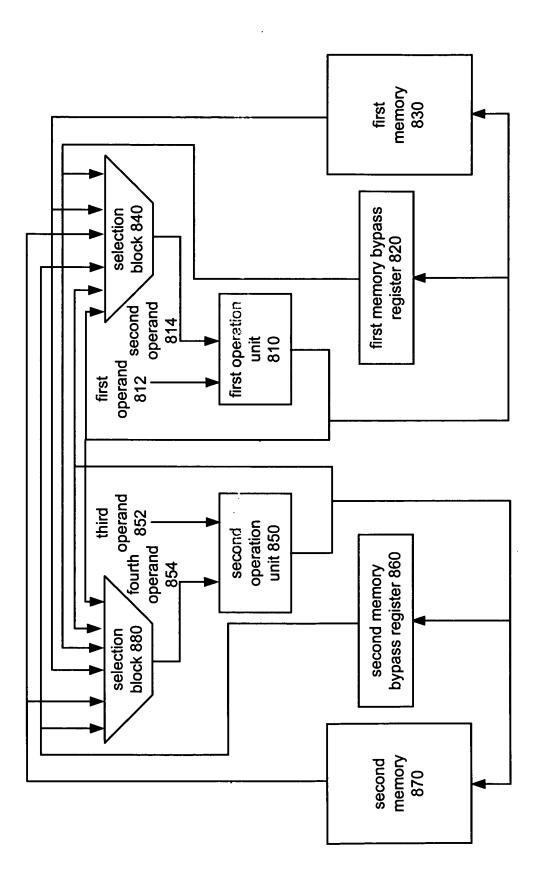


FIG. 15